Meeting Notes

***Meeting Agenda*** –Meeting with Chris to make our circuit diagram on LTspice.

***Meeting Attendance:***

Balaji Radhakrishnan (u7322353)

Bhargav Ashok (u7172176)

The other team member’s presence was not necessary since the physical circuit is being developed by the electrical team.

|  |  |
| --- | --- |
| ***Meeting Notes:*** | ***Meeting Duration ~ 120 minutes*** |

The agenda for the meeting was the discuss the circuit we had developed and obtain feedback. Initially we used a software MPLAB to create the circuit of the system, however on discussion with Chris we observed some shortcomings in viewing the results (primarily the current flowing through the circuit). Therefore, we recreated the circuit with the help of Chris on LTspice software. The main aspect was the recreate the chip used to limit the current on LTspice as the chips model was not there and had to be created manually. At the end of the meeting, we were able to create a working circuit with certain components, however the components availability needs to be verified.

***Meeting Outcomes:***

1. The 2nd iteration of the circuit on LTspice was successfully completed.
2. Key components were identified and modelled on the software.

***Current Deliverables1:***

Identify the list of components required and create an initial (version 1) Bill of Materials List

Verify and simulate using the software that the current flowing through the panels does not exceed the limit set by the client (in our case that would be 10A)